

| Form PTO-1449   |   |      |            |               | ATTY. DOCKET NO.<br>1215-0485P(000277-078)                         |              | APPLICATION NO.<br>10/648,206 |    |
|---|---|------|------------|---------------|--|--------------|-------------------------------|----|
| <b>INFORMATION DISCLOSURE CITATION<br/>IN AN APPLICATION</b><br>(Use several sheets if necessary)   |   |      |            |               | APPLICANT<br>Alfred P. TURLEY                                      |              |                               |    |
|   |   |      |            |               | FILING DATE<br>August 27, 2003                                     |              | GROUP<br>2811                 |    |
| <b>U.S. PATENT DOCUMENTS</b>  |   |      |            |               |  |              |                               |    |
| EXAMINER<br>INITIAL   | DOCUMENT NUMBER   | Kind | DATE       | NAME          | CLASS  | SUB<br>CLASS | FILING DATE<br>IF APPROPRIATE |    |
| Loke  | US 2001/0008284   | A1   | 2001-07-19 | HUANG         |  |              |                               |    |
| Loke  | US 2002/0053705   | A1   | 2002-05-09 | Kondo et al.  |  |              |                               |    |
| Loke  | US 2002/0142558   | A1   | 2002-10-03 | Hsu           |  |              |                               |    |
| Loke  | US 2003/0057487   | A1   | 2003-03-27 | Yamada et al. |  |              |                               |    |
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| <b>FOREIGN PATENT DOCUMENTS</b>   |   |      |            |               |  |              |                               |    |
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|   |   |      |            |               |  |              | YES                           | NO |
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| <b>OTHER DOCUMENTS</b> (Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.) |   |      |            |               |  |              |                               |    |
| Loke  | "A 3-D BiCMOS Technology Using Selective Epitaxial Growth (SEG) and Lateral Solid Phase Epitaxy (LSPE), Kumar et al., International Electron Devices Meeting, 2001, Technical Digest, Washington, D.C. December 2-5, 2001, New York, New York; IEEE, US December 2, 2001, pages 729-732 |      |            |               |  |              |                               |    |
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| EXAMINER<br><div style="text-align: center;">Loke</div>   |   |      |            |               | DATE CONSIDERED<br><div style="text-align: center;">10/29/04</div> |              |                               |    |
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